

Fig. 1

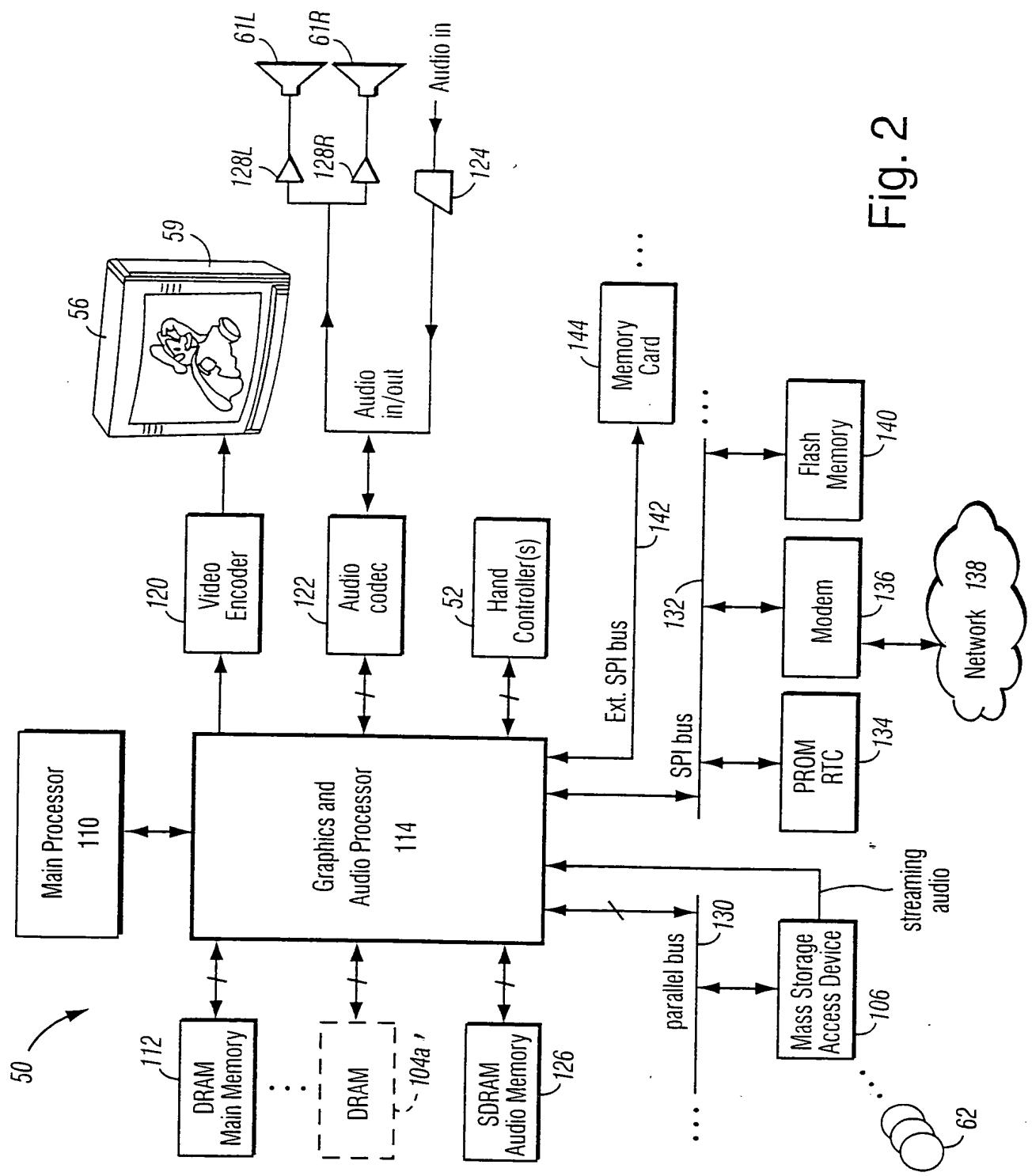
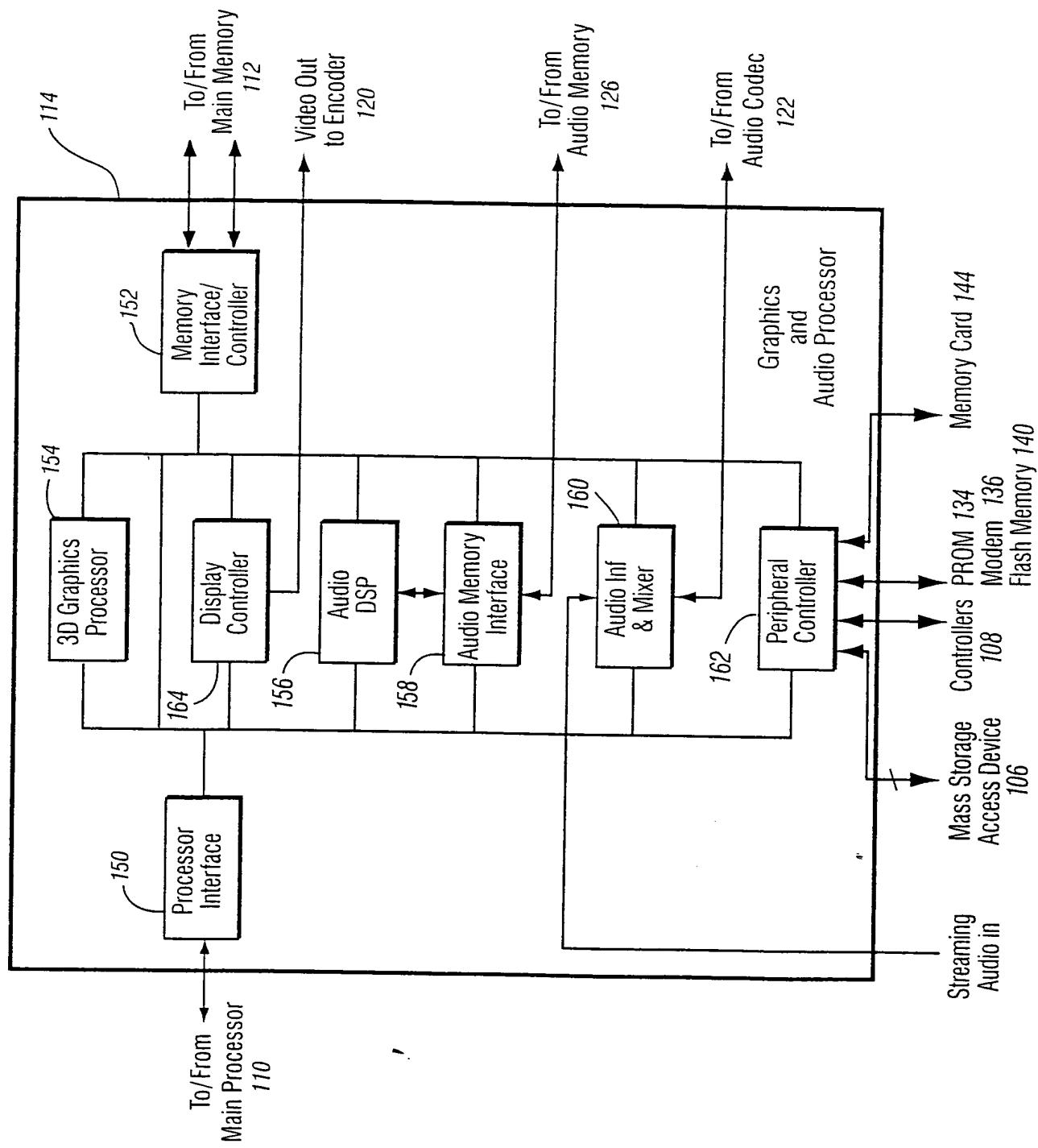


Fig. 2

Fig. 3



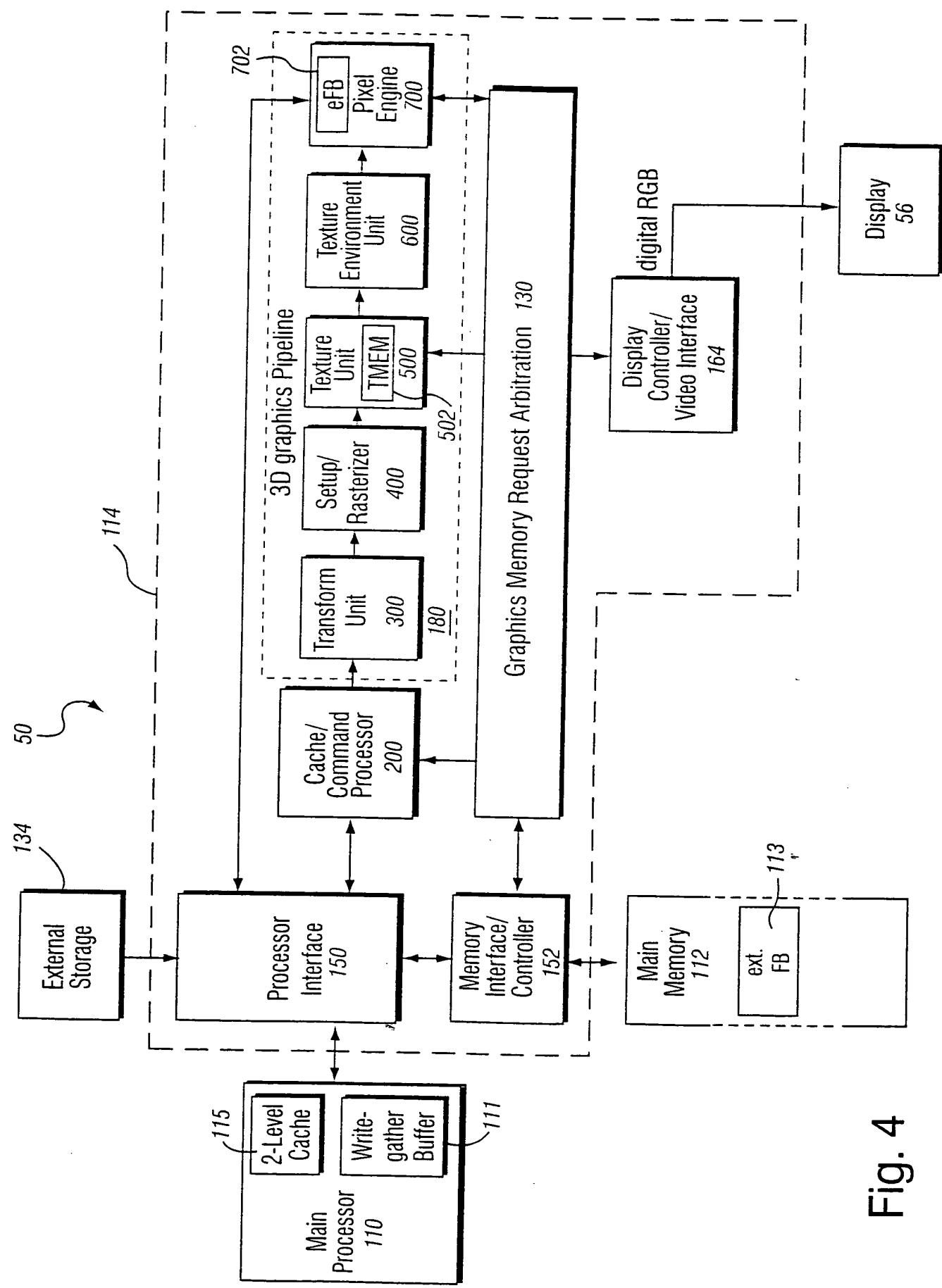


Fig. 4

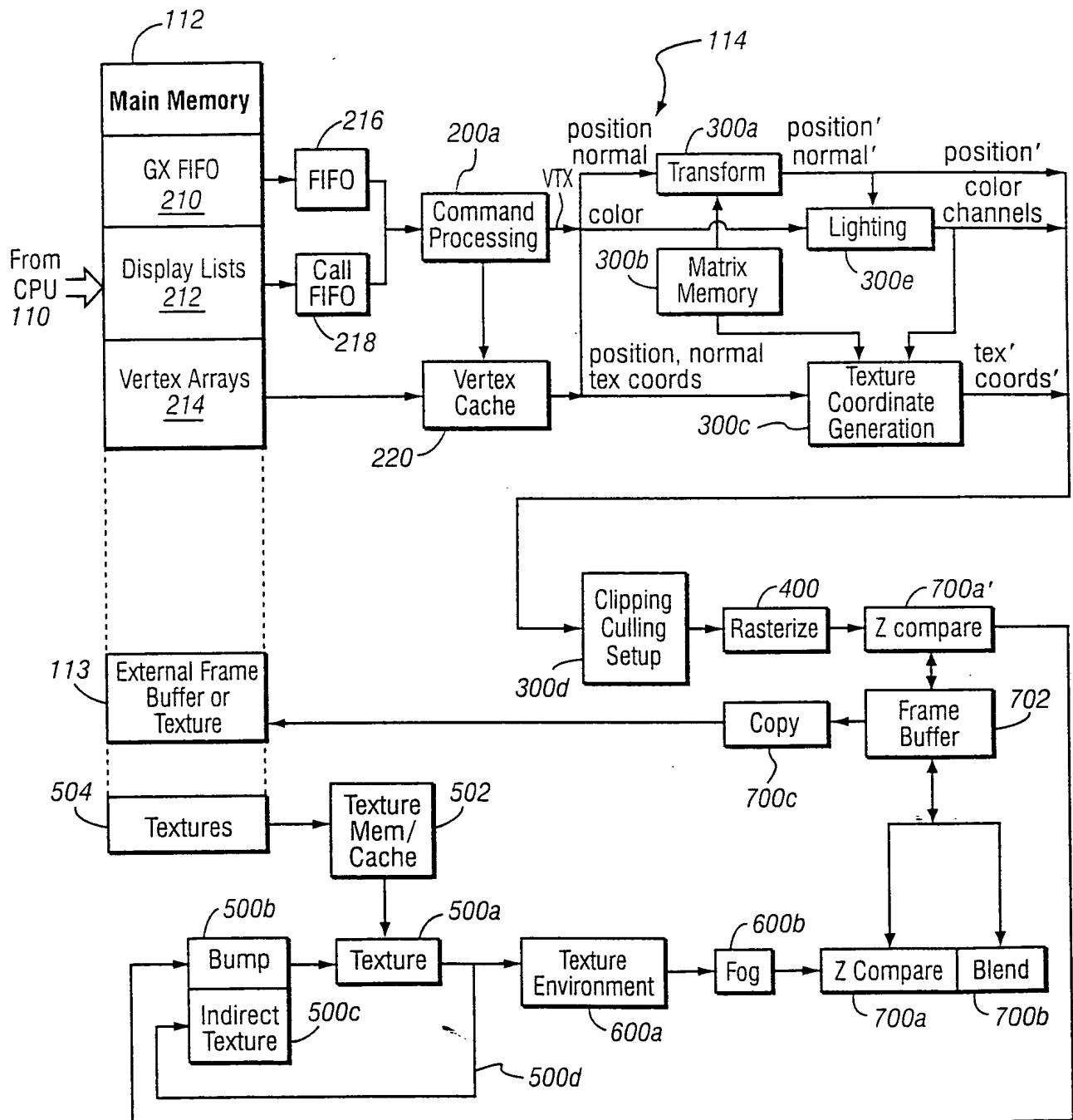


Fig. 5 EXAMPLE GRAPHICS PROCESSOR FLOW

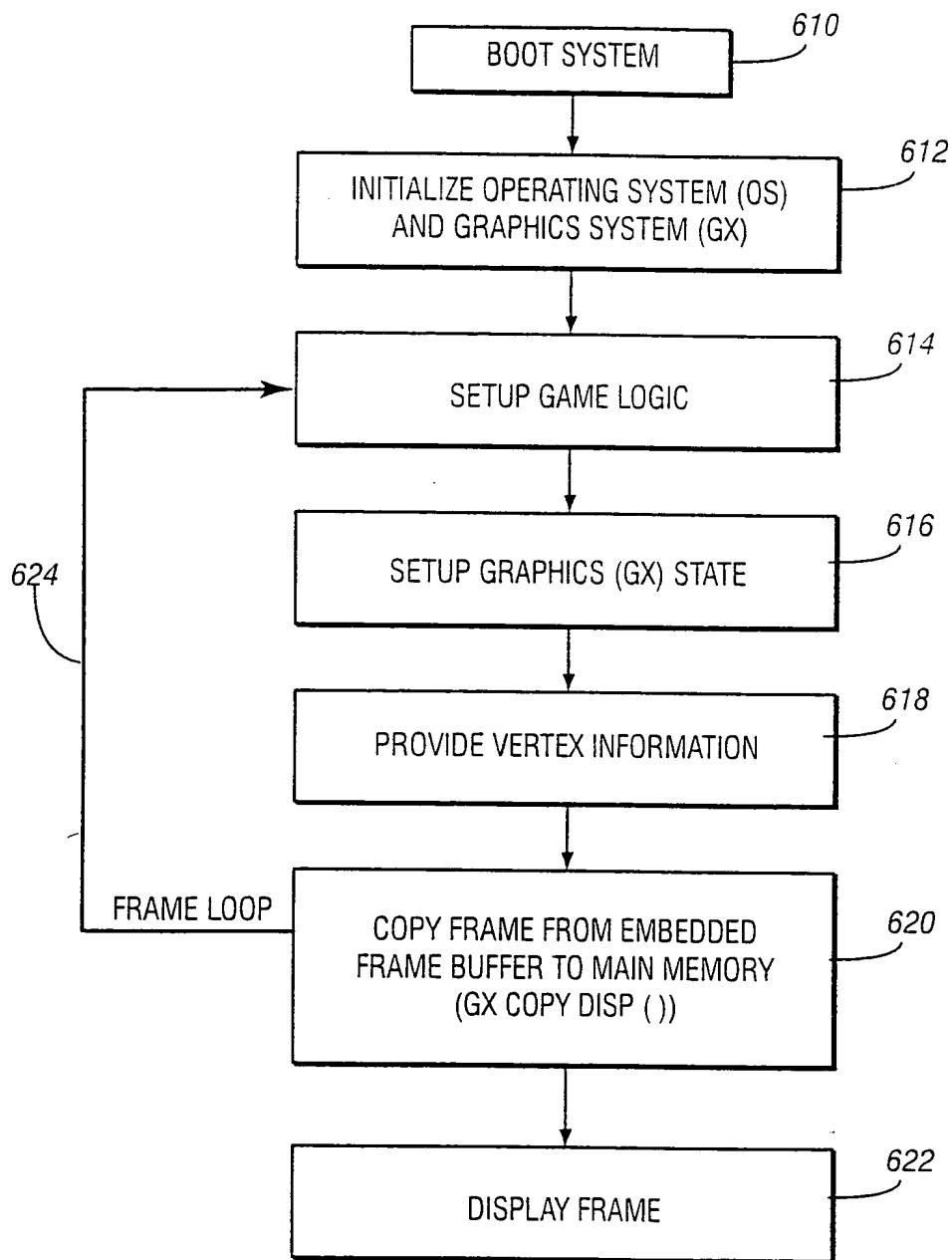
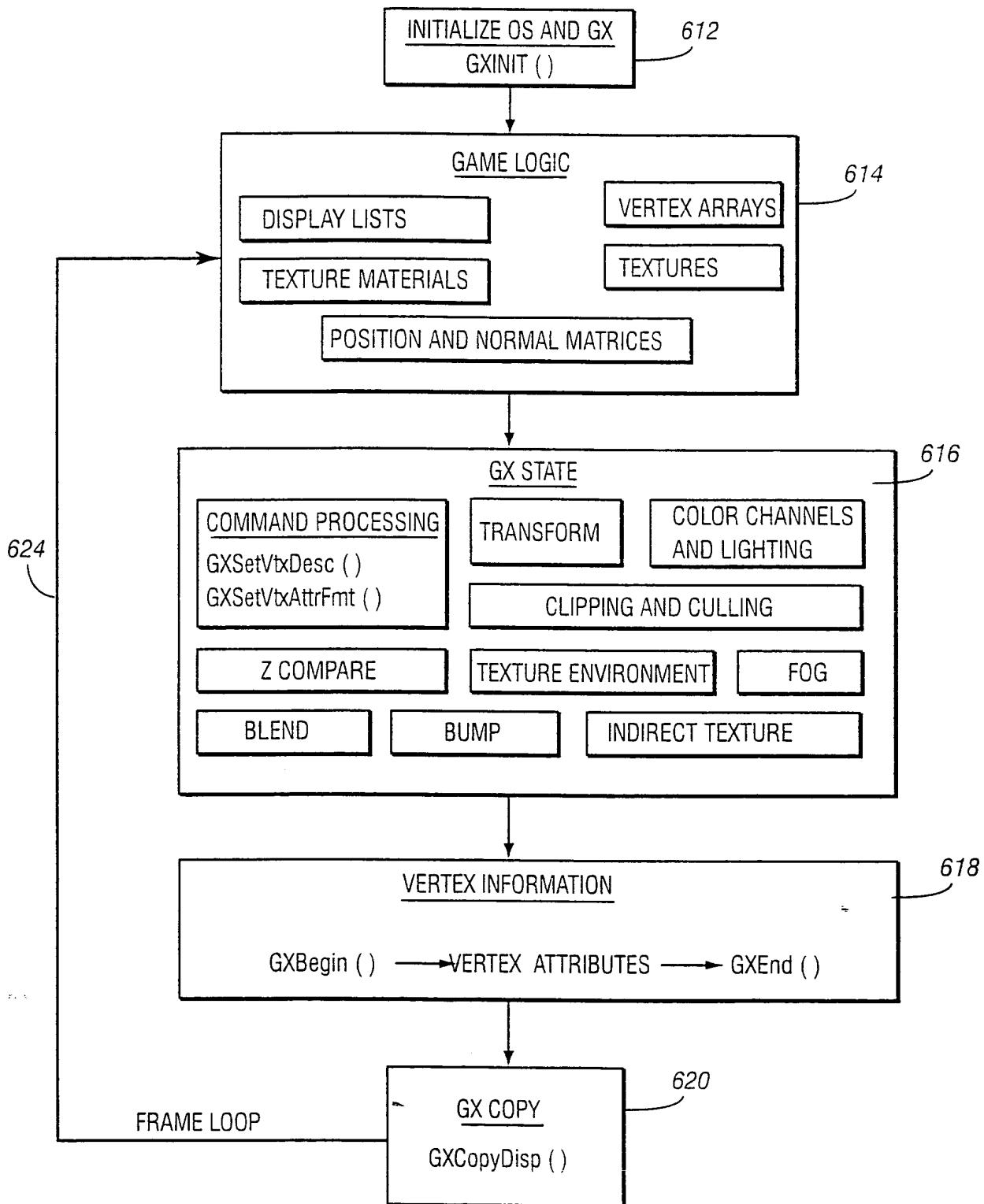
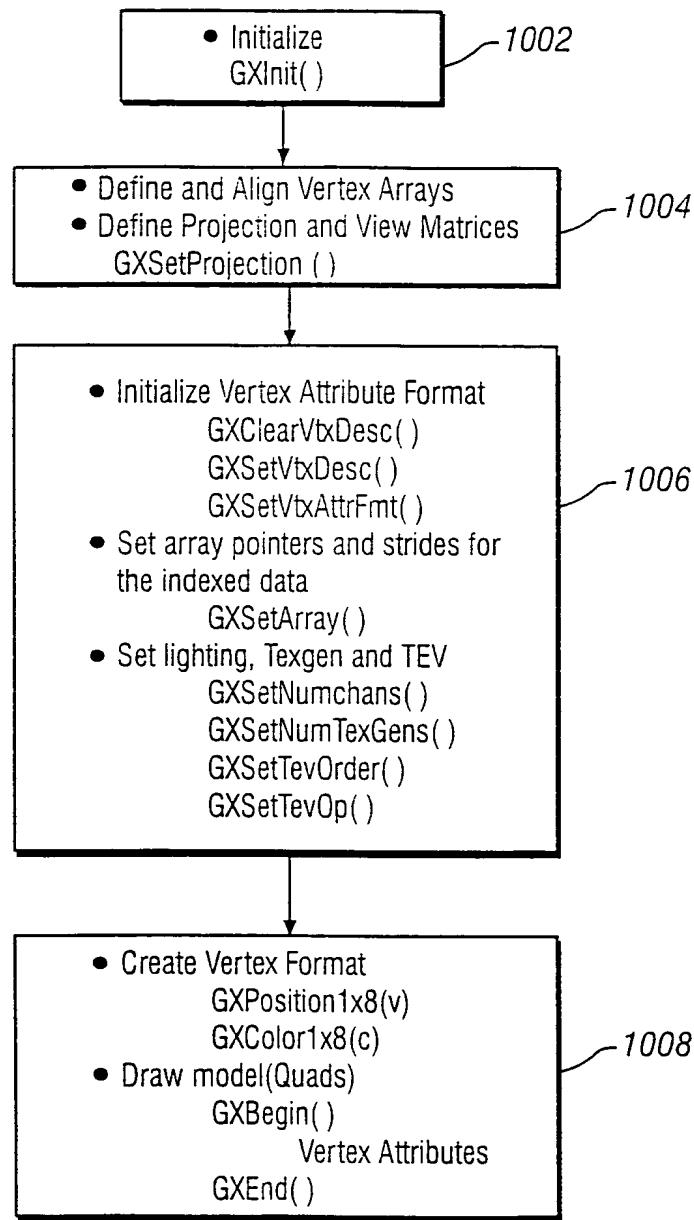


Fig. 6
EXAMPLE GRAPHICS
PROCESSING LOOP



EXAMPLE MORE DETAILED GRAPHIC
PROCESSING LOOP

Fig. 7



EXAMPLE SIMPLE GRAPHIC APPLICATION

Fig. 8

EXAMPLE GX SET COPY CLEAR (BLACK,0x00FFFFFF):

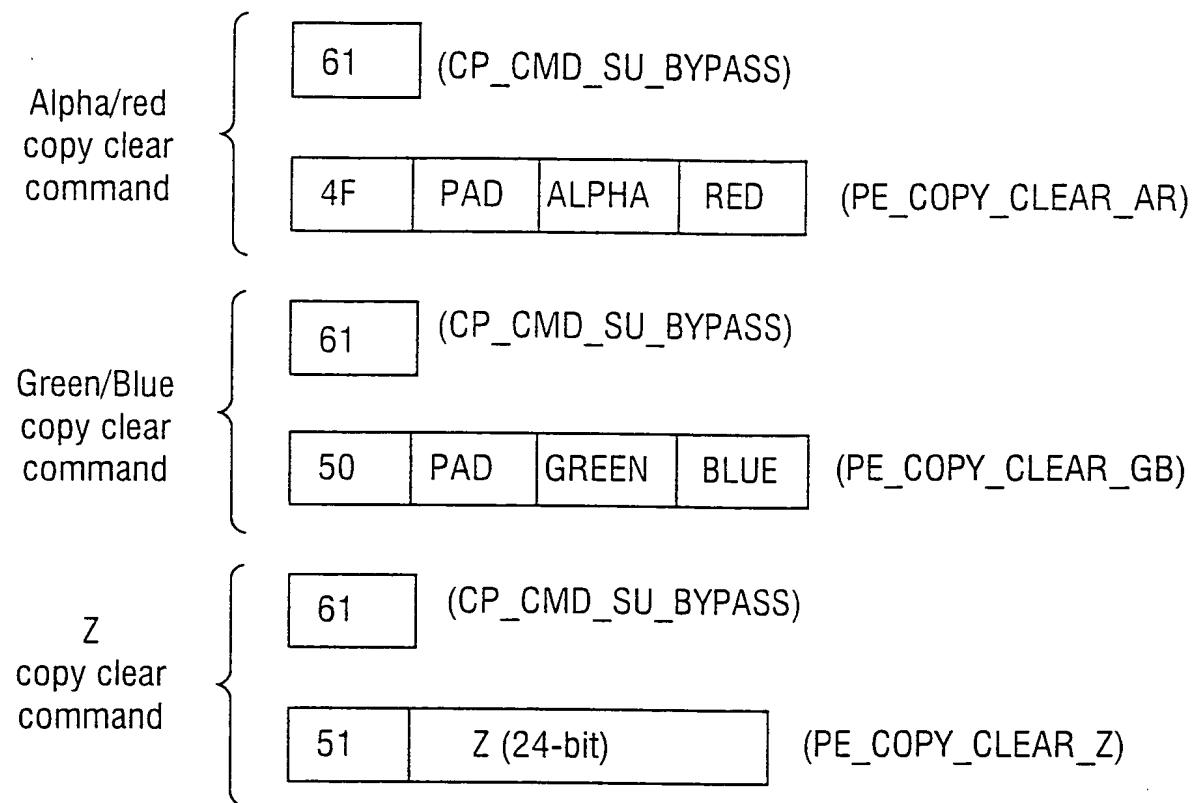
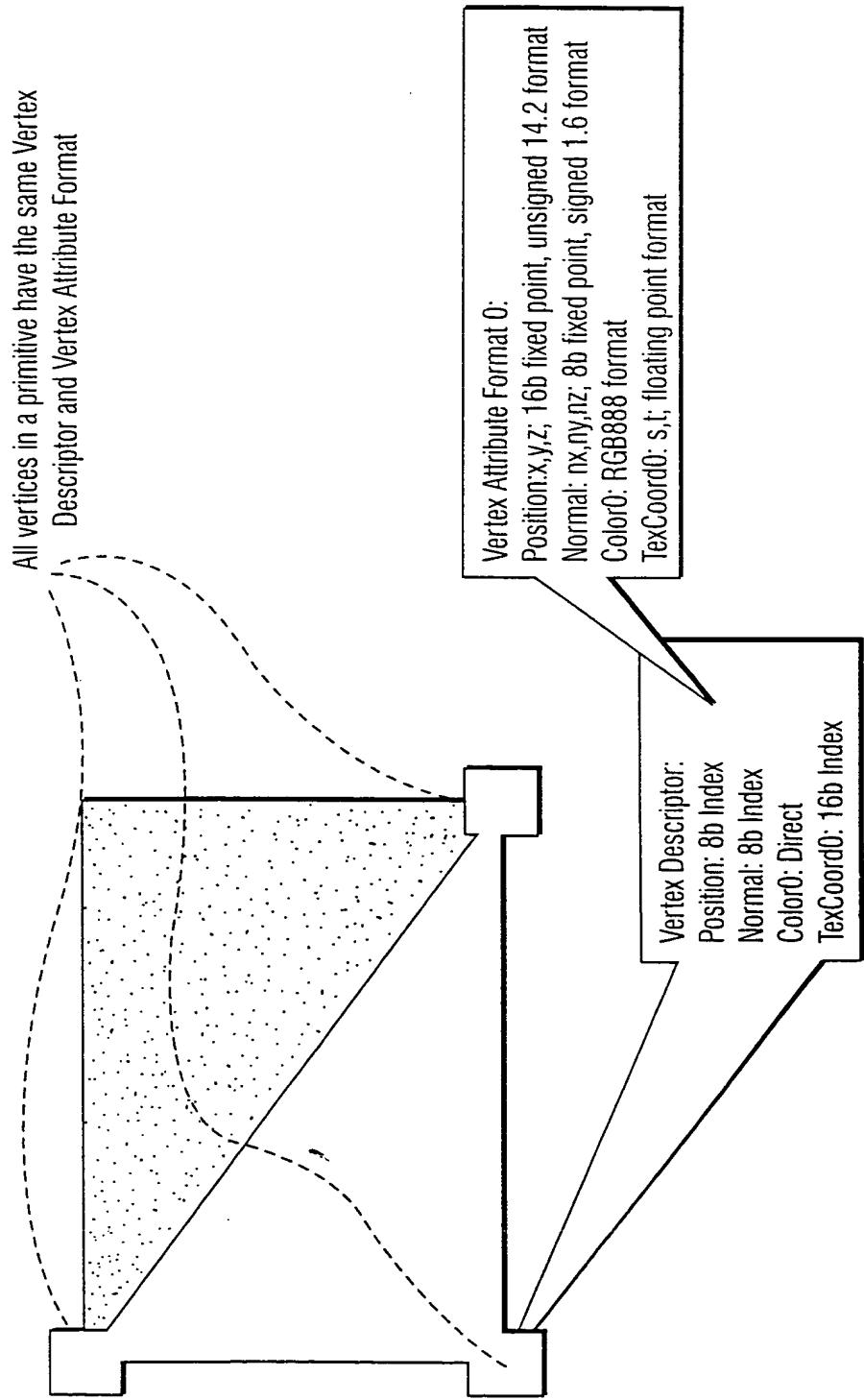


Fig. 9

0x4f: copy_color_ar	23	1	1	1	15	1	1	1	1	7	1	1	1	1
red and alpha component of the color value written to efb during a copy command.														
(7:0) reg														
(15:8) alpha	23	1	1	1	15	1	1	1	1	7	1	1	1	1
0x50: copy_color_gb	23	1	1	1	15	1	1	1	1	7	1	1	1	1
red and alpha component of the color value written to efb during a copy command.														
(7:0) blue														
(15:8) green														
0x51: copy_clear_z	23	1	1	1	15	1	1	1	1	7	1	1	1	1
23:0 z value written into the z efb.														

Fig. 9a EXAMPLE PIXEL ENGINE COPY CLEAR
REGISTER FORMATS

Fig. 10 EXAMPLE (VERTEX AND ATTRIBUTE DESCRIPTION)



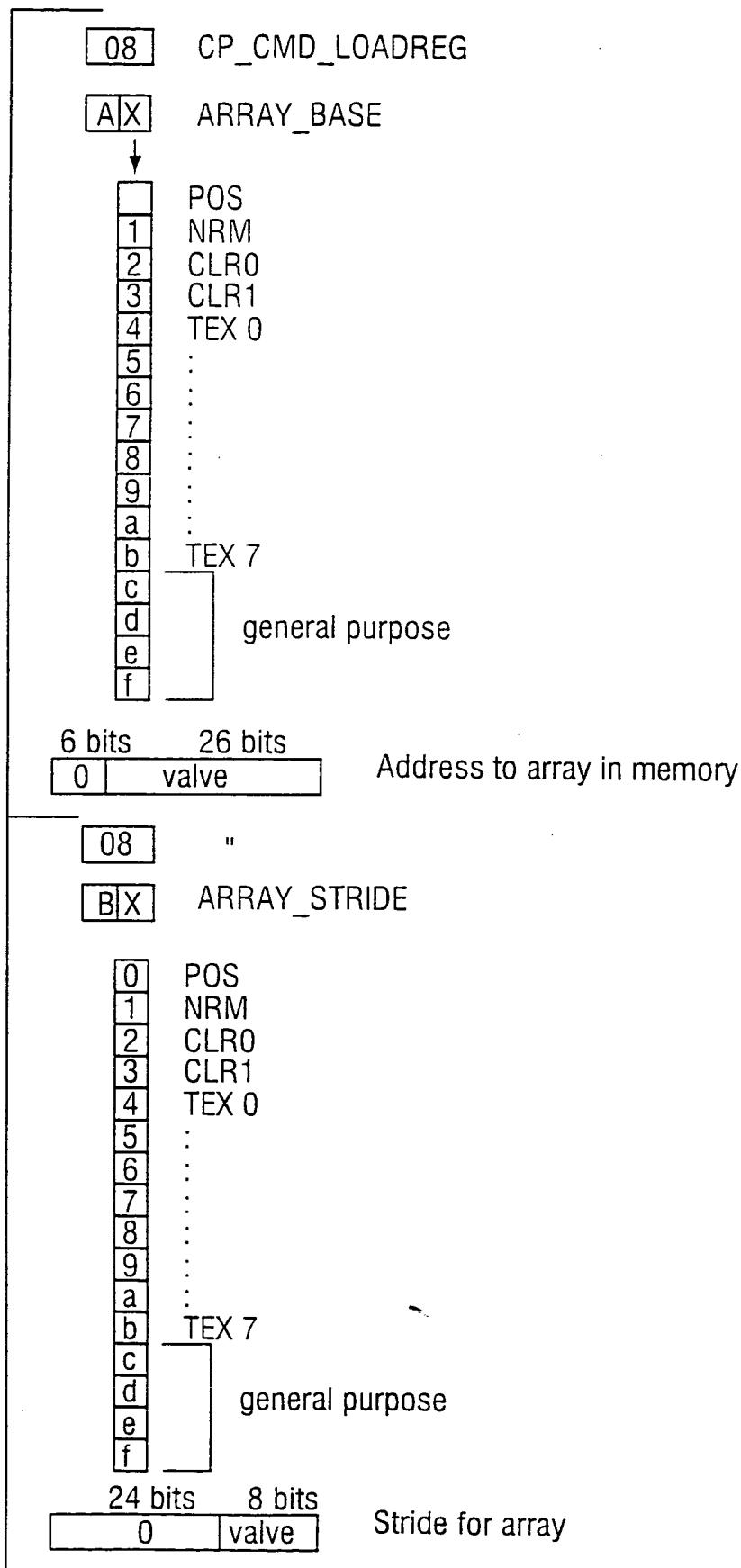


Fig. 11 GX SET ARRAY (GX ATTR, ATTR, VOID + BASE APTR, .8 STRIDE)

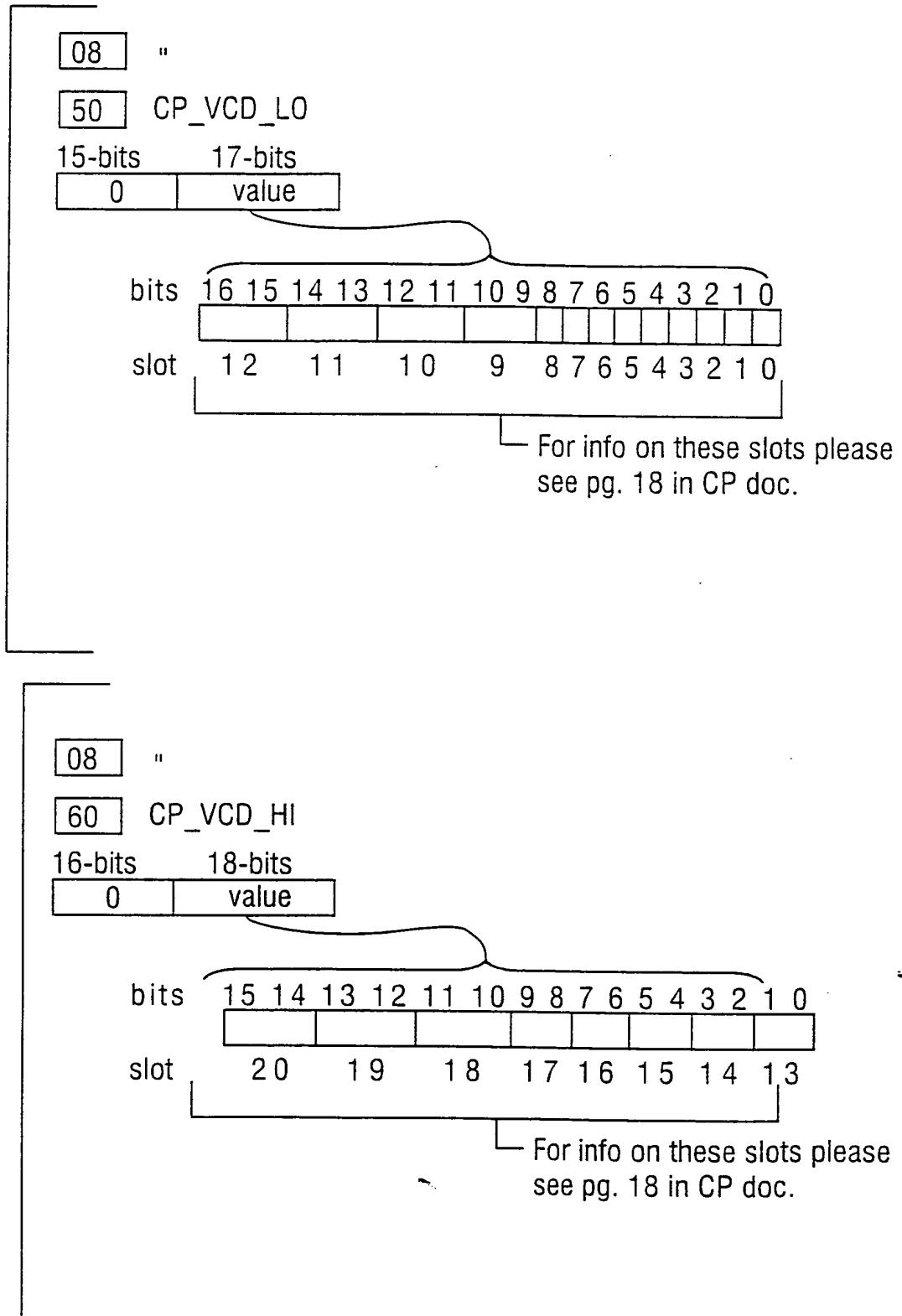


Fig. 12

GX SET VTX ATTR ATTR

GX SET VTX ATTR FMT();

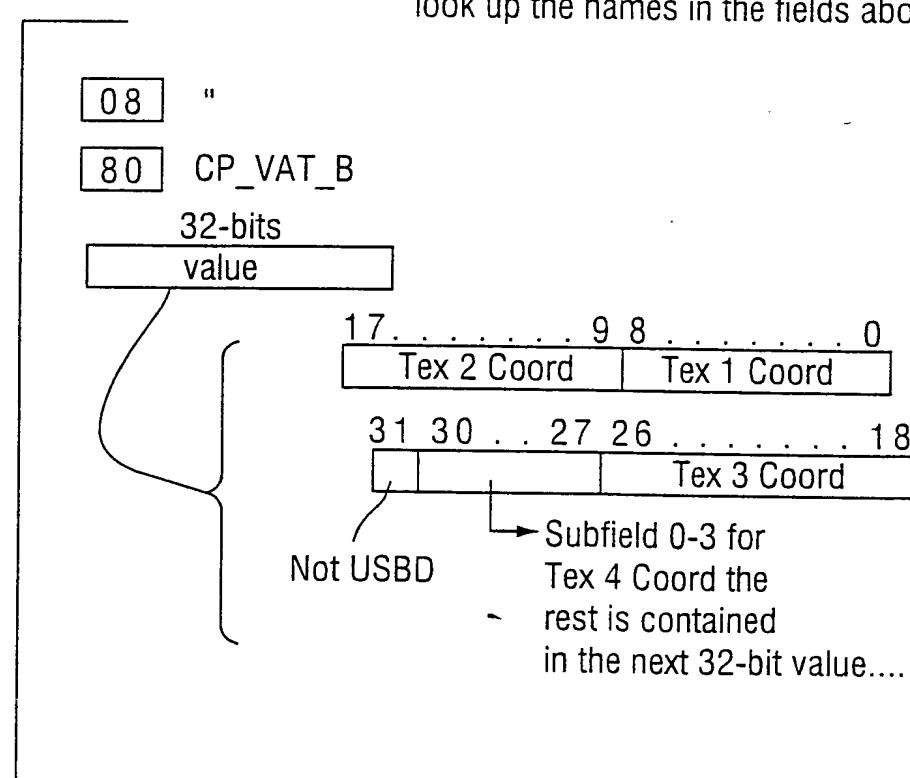
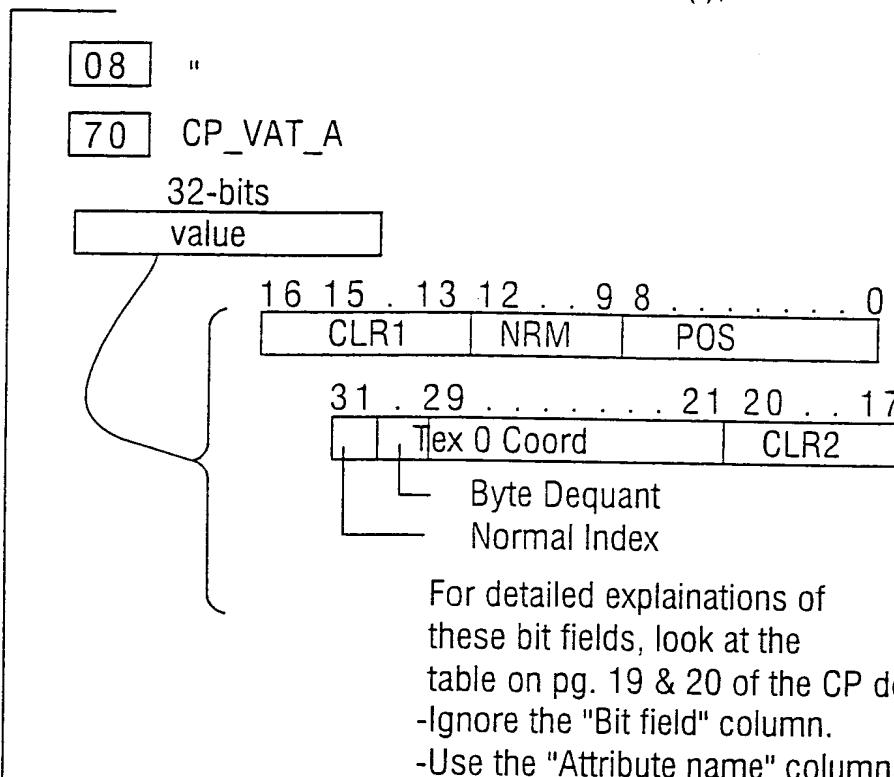
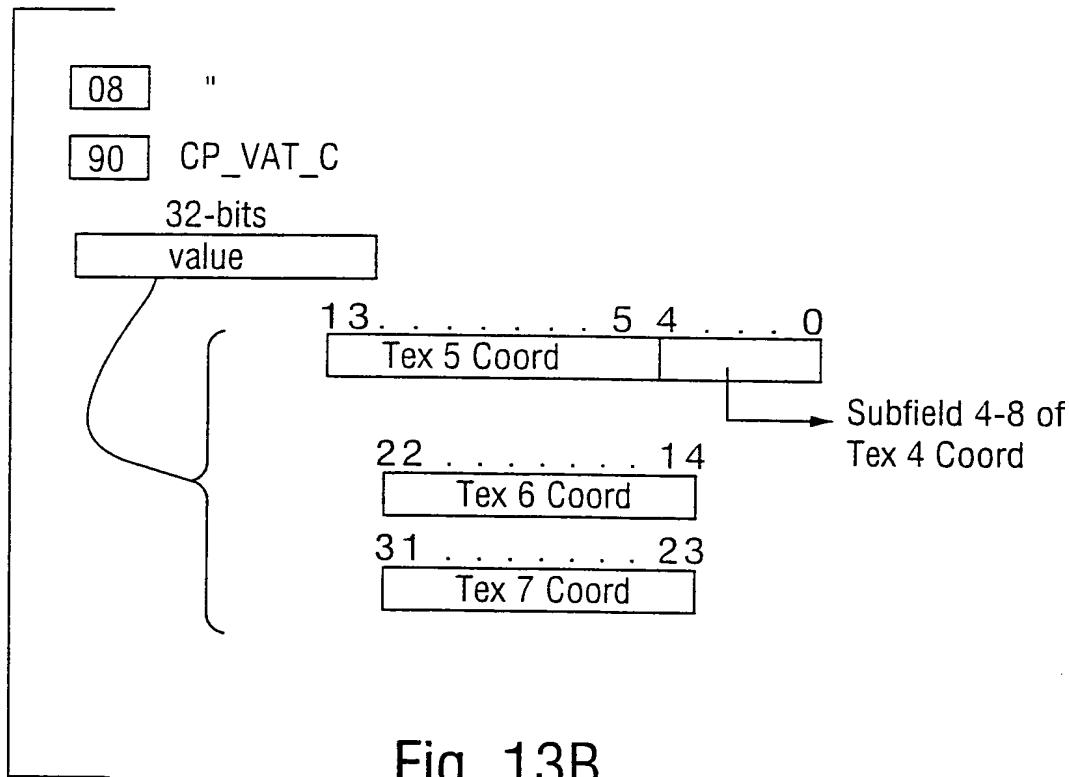


Fig. 13A



	GX_VTXFMT0	GX_VTXFMT1	GX_VTXFMT2	GX_VTXFMT3	GX_VTXFMT4	GX_VTXFMT5	GX_VTXFMT6	GX_VTXFMT7
GX_VA_POS	n elements format/size scale							
GX_VA_NRM								
GX_VA_CLR0								
GX_VA_CLR1								
GX_VA_TEX0								
GX_VA_TEX1	n elements format/size scale							
GX_VA_TEX2								
GX_VA_TEX3								
GX_VA_TEX4								
GX_VA_TEX5								
GX_VA_TEX6								
GX_VA_TEX7								

Fig. 14 (VERTEX ATTRIBUTE FORMAT TABLE)

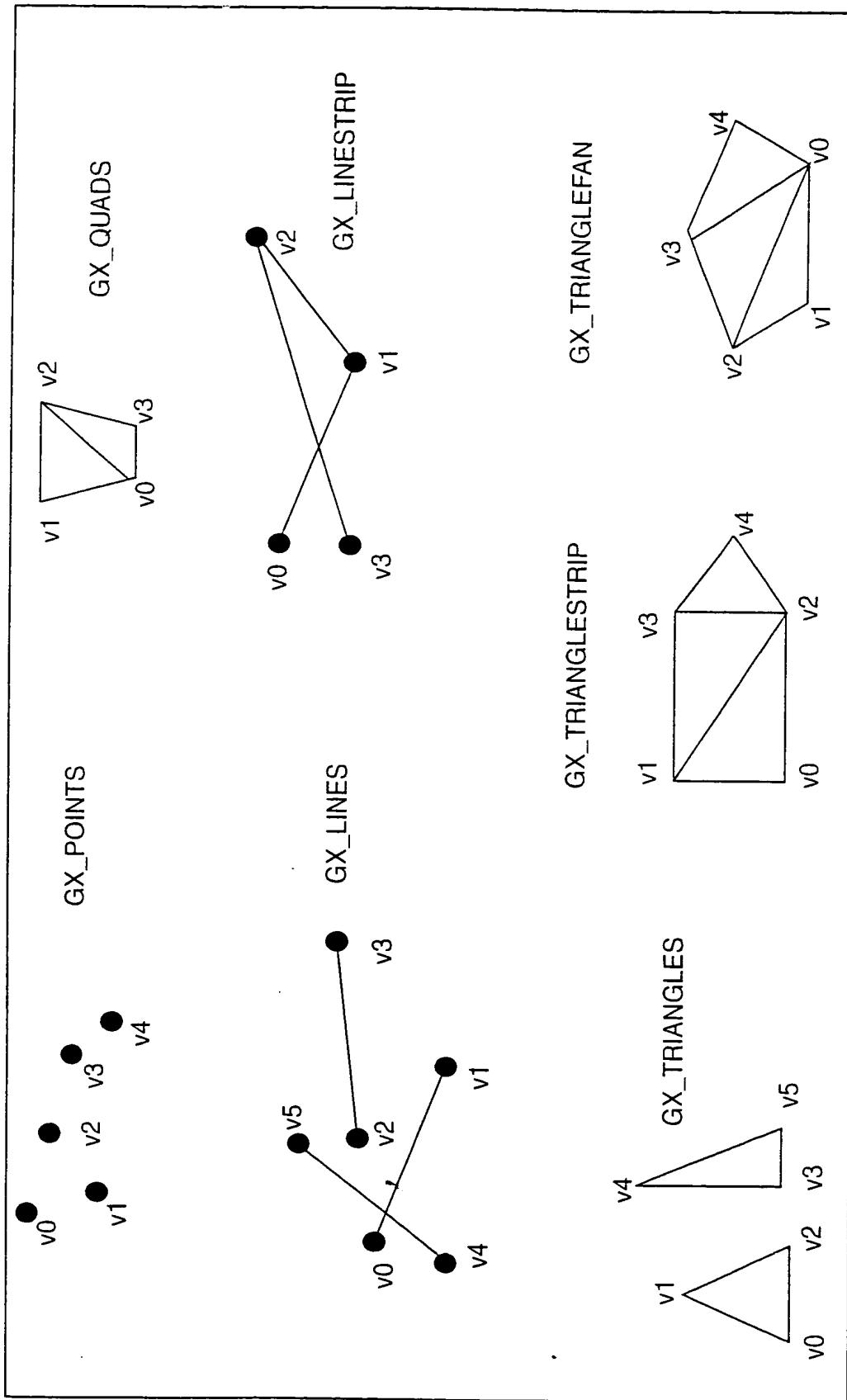


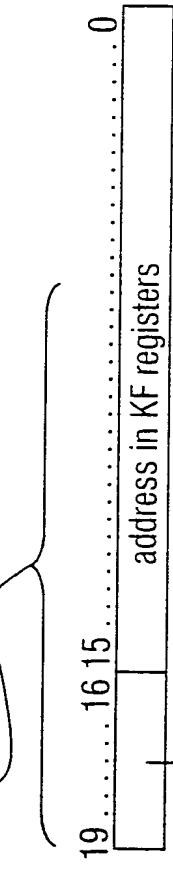
Fig. 15 EXAMPLE (GRAPHICS PRIMITIVES)

GK set Projection ()

10 CP_CMD_KF_LOADREGS

11 bits 21 bits

0 | value



19 16 15 0
address in KF registers
N + 1 (0 => 1, 0xff => 16)

32 bit words will follow.

In this specific example the projection matrix
is @ address 0x1020 and 7 32-bit words
will follow.

6 0x1020

Hence,

Proj A
Proj B
Proj C
Proj D
Proj E
Proj F
Proj G

Fig. 16

GX Call Display List ()

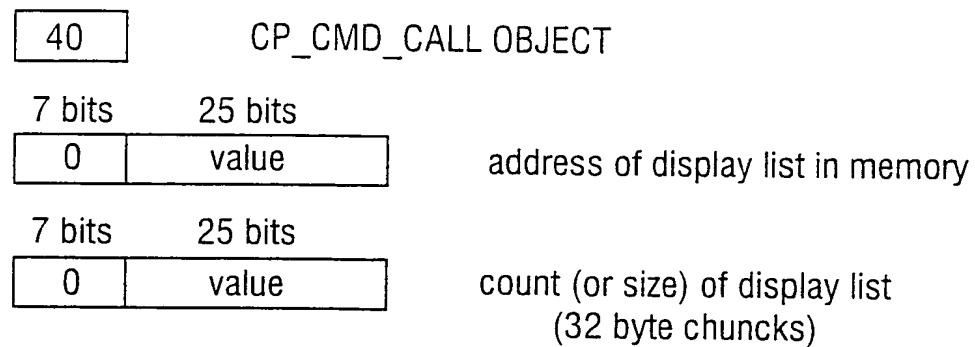


Fig. 17

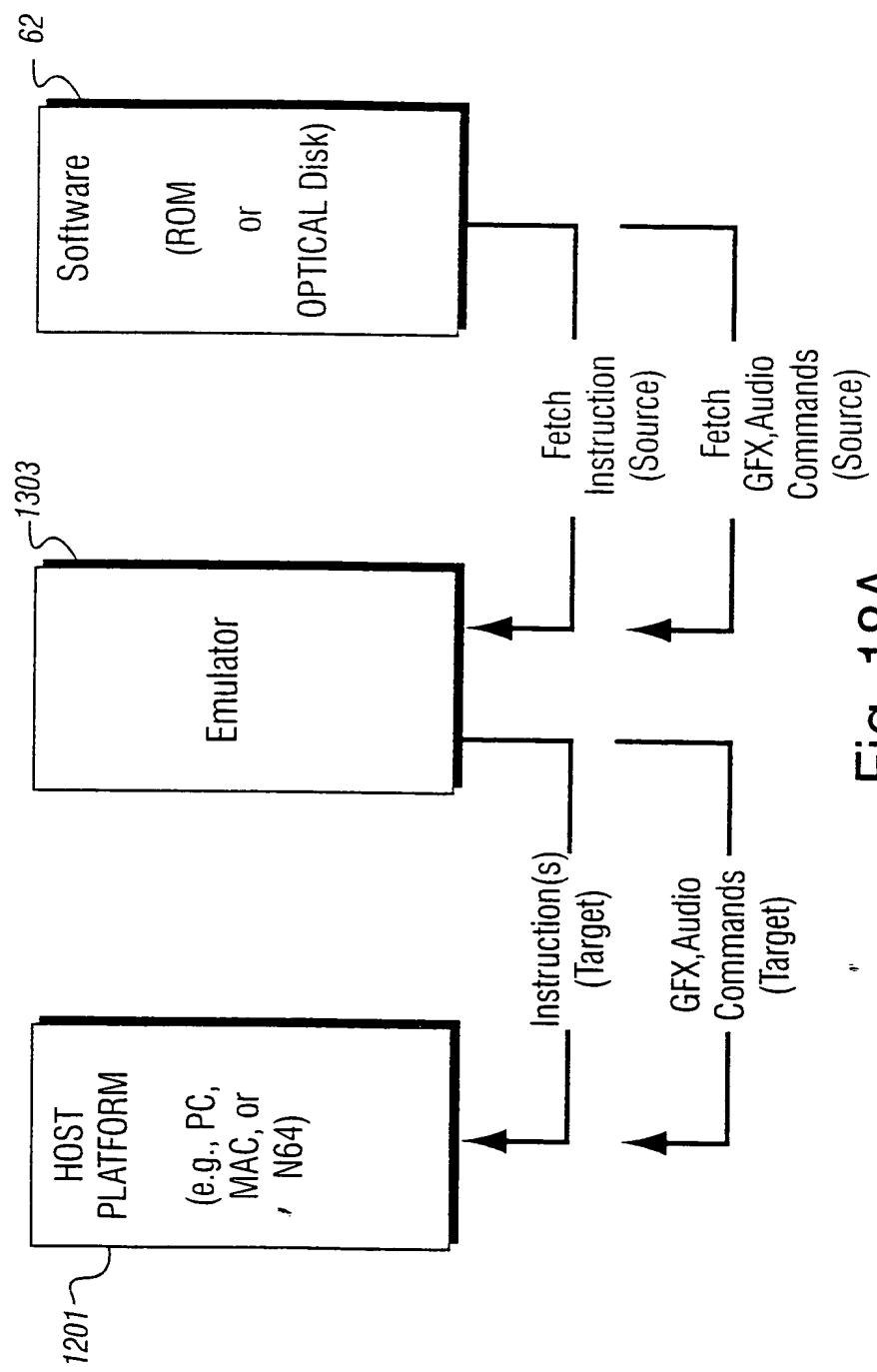


Fig. 18A

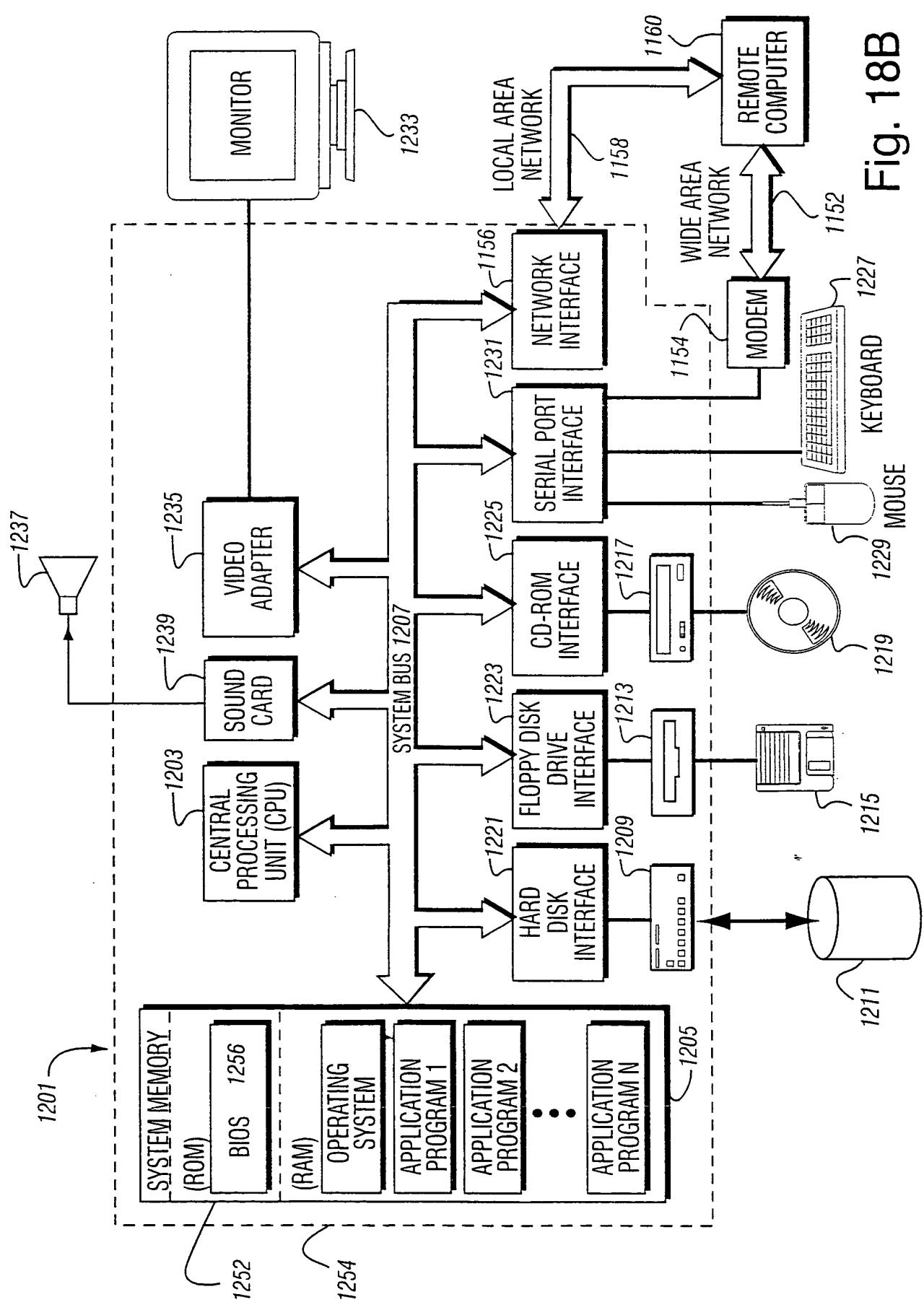


Fig. 18B